

PATENT  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

FUKAMI, Teruaki

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SILICON WAFER STORAGE METHOD

Art Unit: NOT ASSIGNED

Examiner: NOT ASSIGNED

ASSOCIATE POWER OF ATTORNEY (37 C.F.R. 1.34)

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I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

SIGNATURE OF INVENTOR 1 <i>Teruaki Fukami</i>	SIGNATURE OF INVENTOR 2
DATE <i>October 26, 1998</i>	DATE
SIGNATURE OF INVENTOR 3	SIGNATURE OF INVENTOR 4
DATE	DATE

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